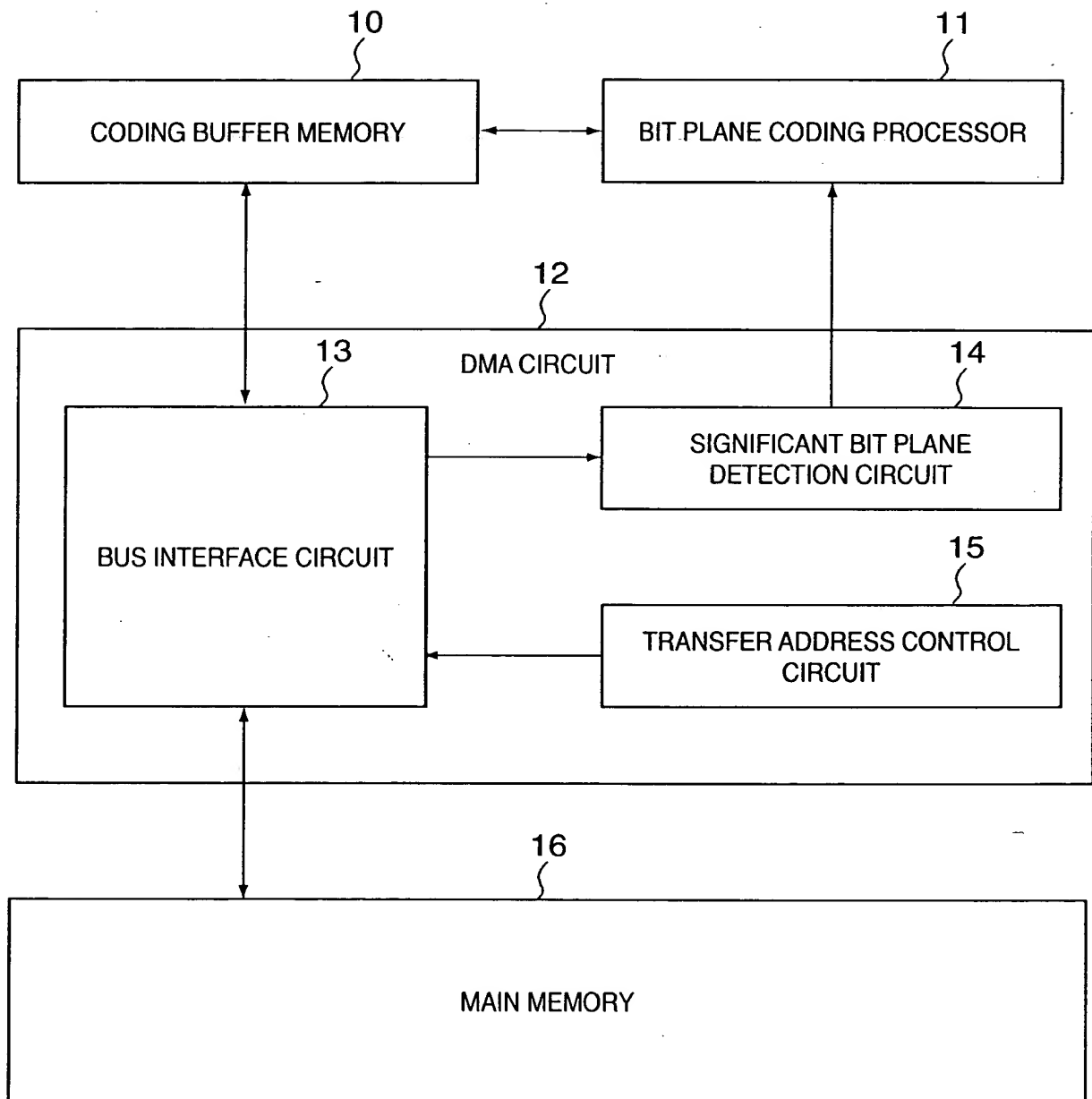


FIG. 1



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FIG. 2

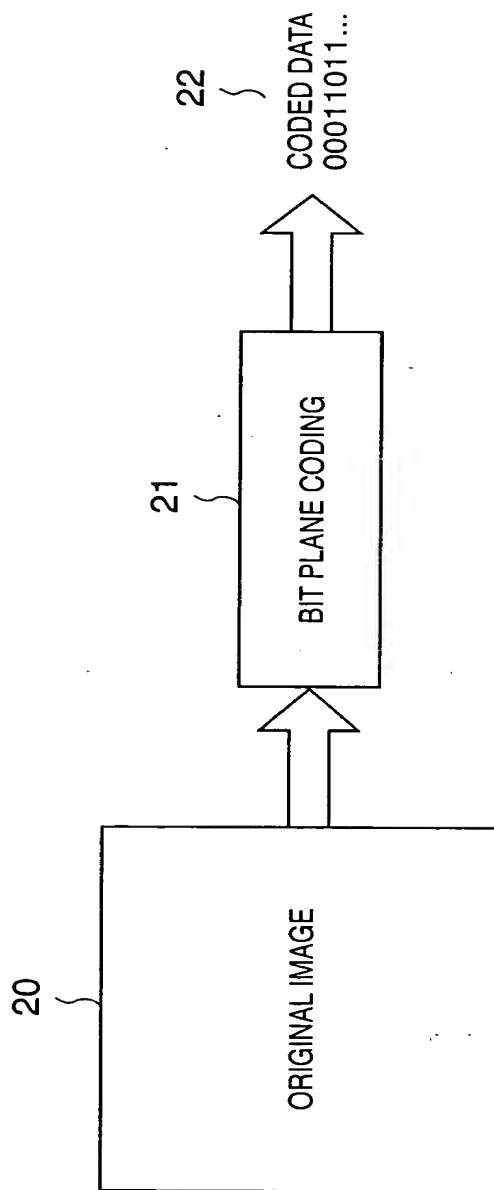


FIG. 3

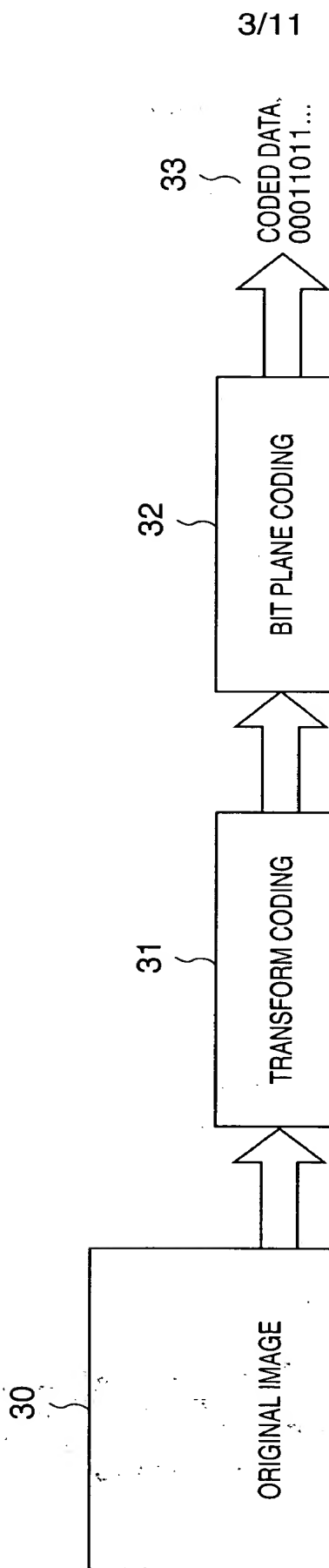


FIG. 4

PIXEL VALUE ( OR COEFFICIENT VALUE ) ARRAY

12	5	5	12
12	12	5	5
11	12	12	12
7	11	12	12

40

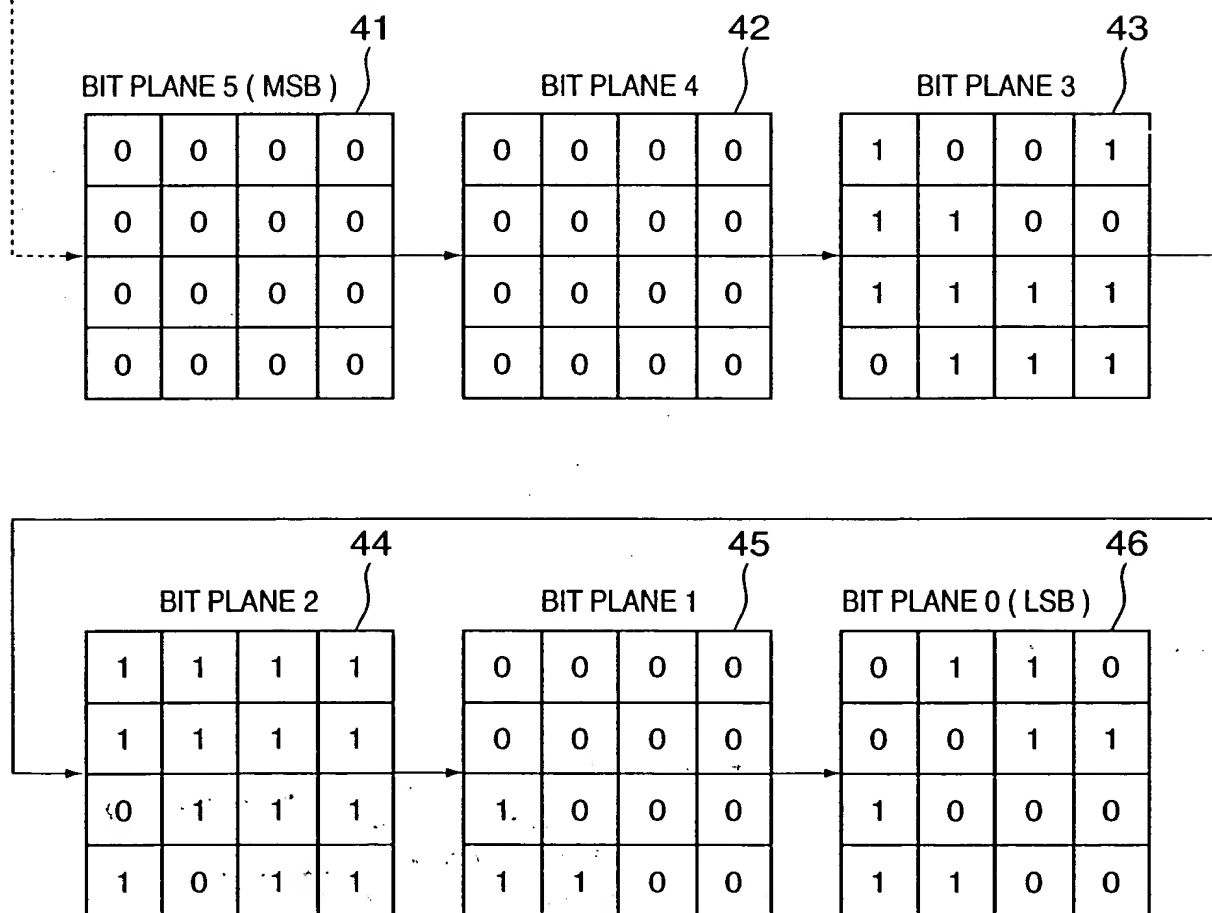


FIG. 5

PIXEL VALUE ( OR COEFFICIENT VALUE ) ARRAY

50

12	5	5	12
12	12	5	5
11	12	12	12
7	11	12	12

SIGNIFICANT BIT  
PLANE DETECTION  
CIRCUIT

14

SIGNIFICANT BIT  
PLANE START  
POSITION = 3

52

53  
BIT PLANE 5 (MSB)

0	0	0	0
0	0	0	0
0	0	0	0
0	0	0	0

54  
BIT PLANE 4

0	0	0	0
0	0	0	0
0	0	0	0
0	0	0	0

55  
BIT PLANE 3

1	0	0	1
1	1	0	0
1	1	1	1
0	1	1	1

56  
BIT PLANE 2

1	1	1	1
1	1	1	1
0	1	1	1
1	0	1	1

57  
BIT PLANE 1

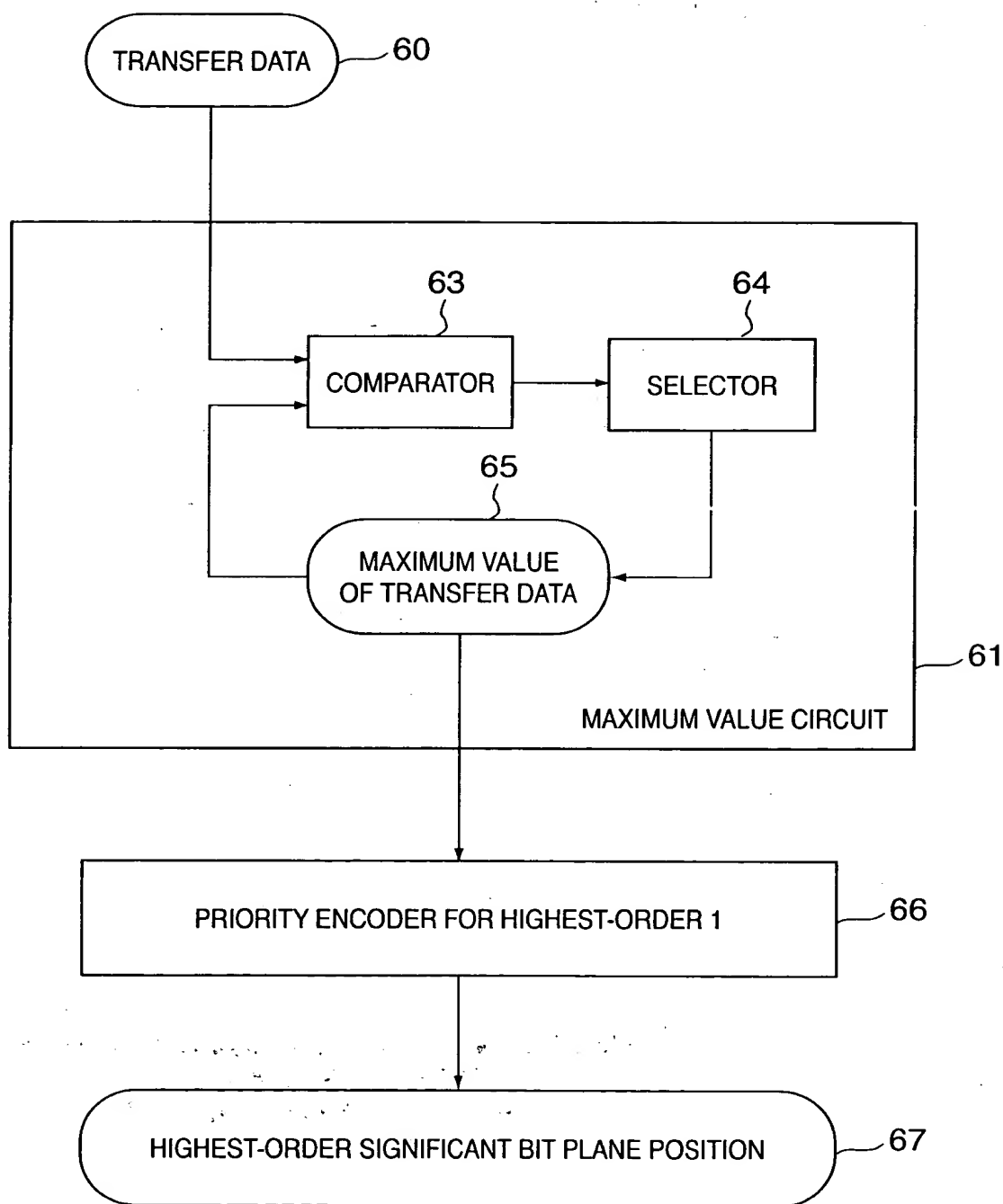
0	0	0	0
0	0	0	0
1	0	0	0
1	1	0	0

58  
BIT PLANE 0 (LSB)

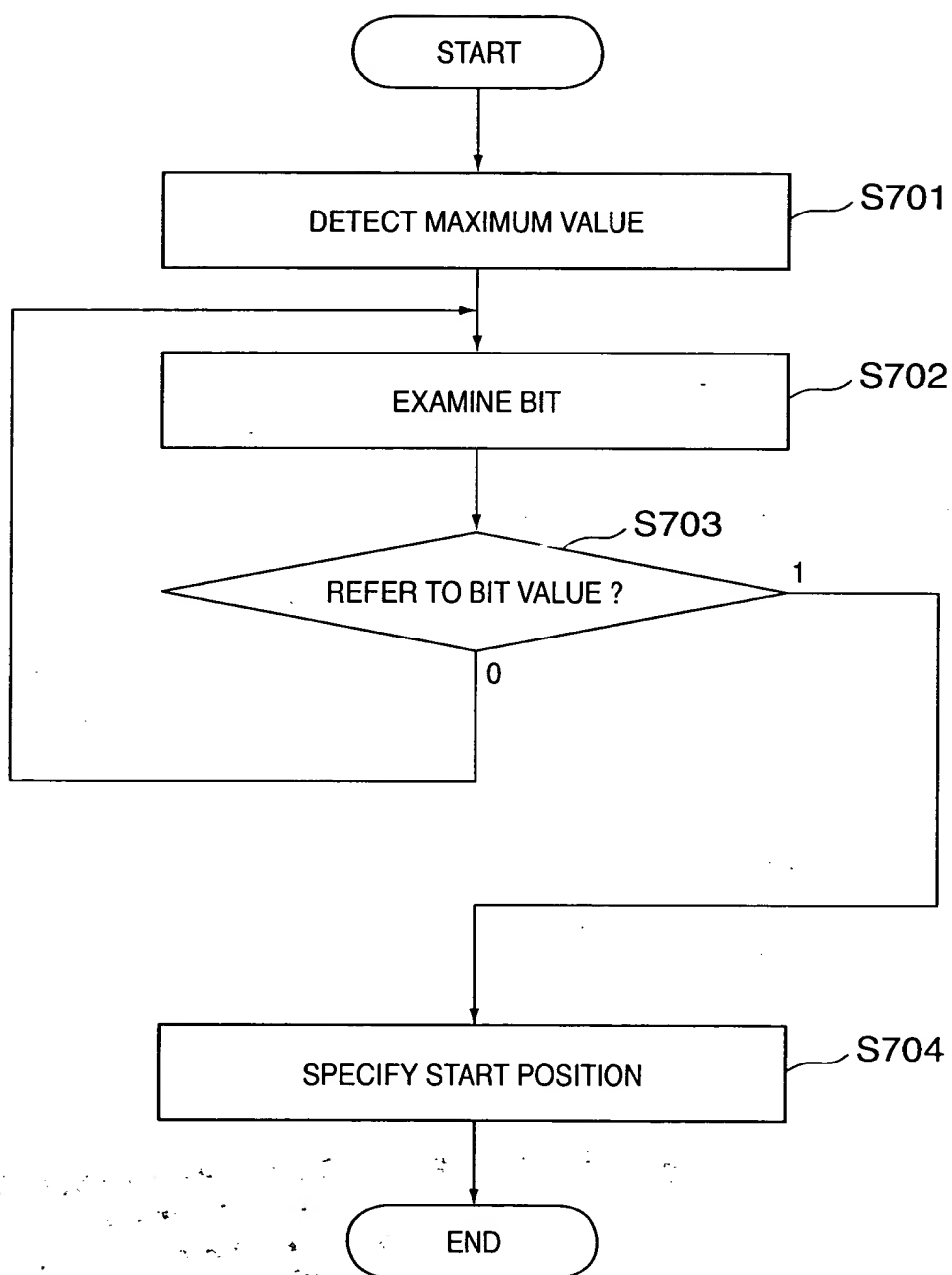
0	1	1	0
0	0	1	1
1	0	0	0
1	1	0	0

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FIG. 6

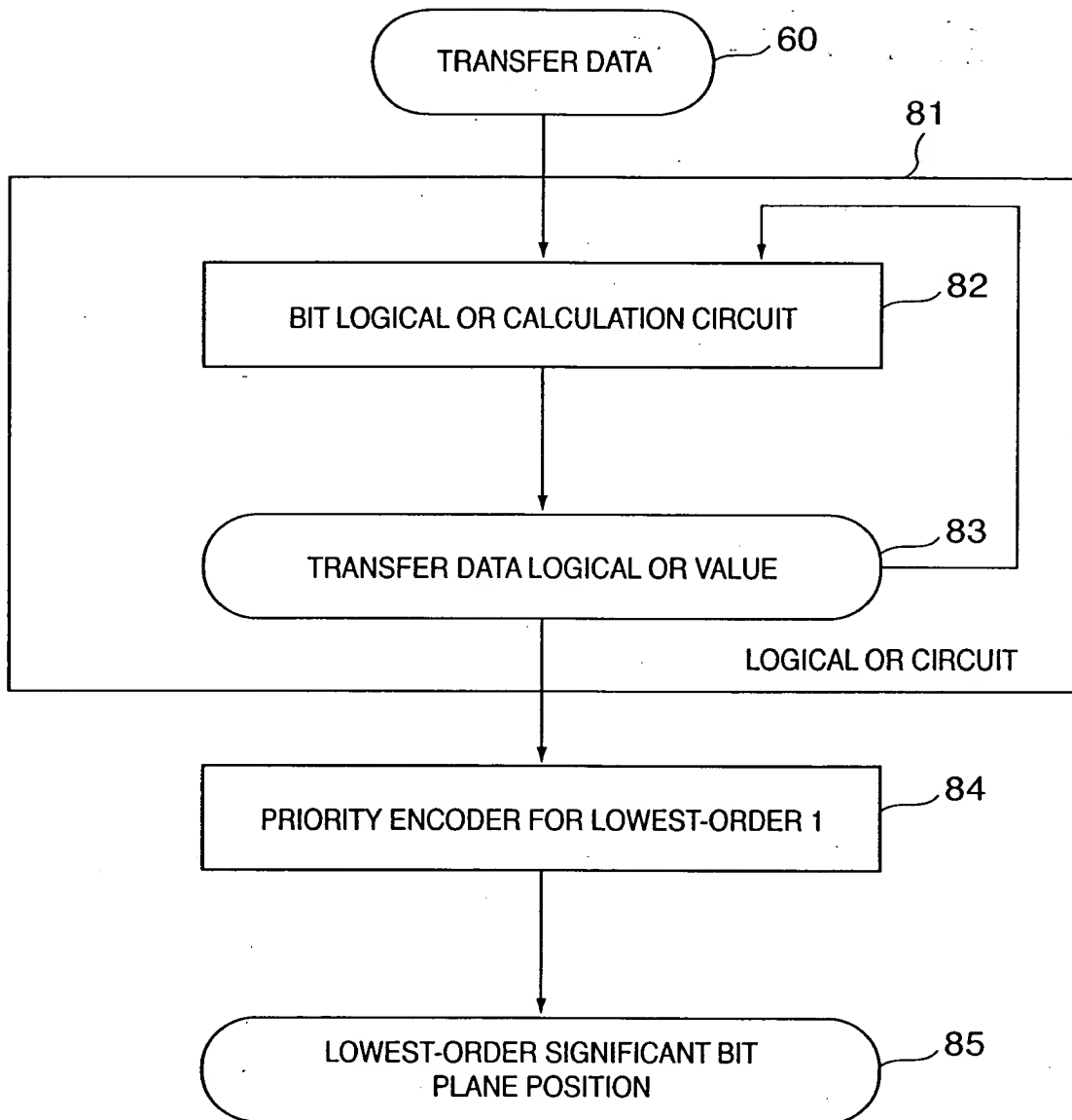


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**FIG. 7**

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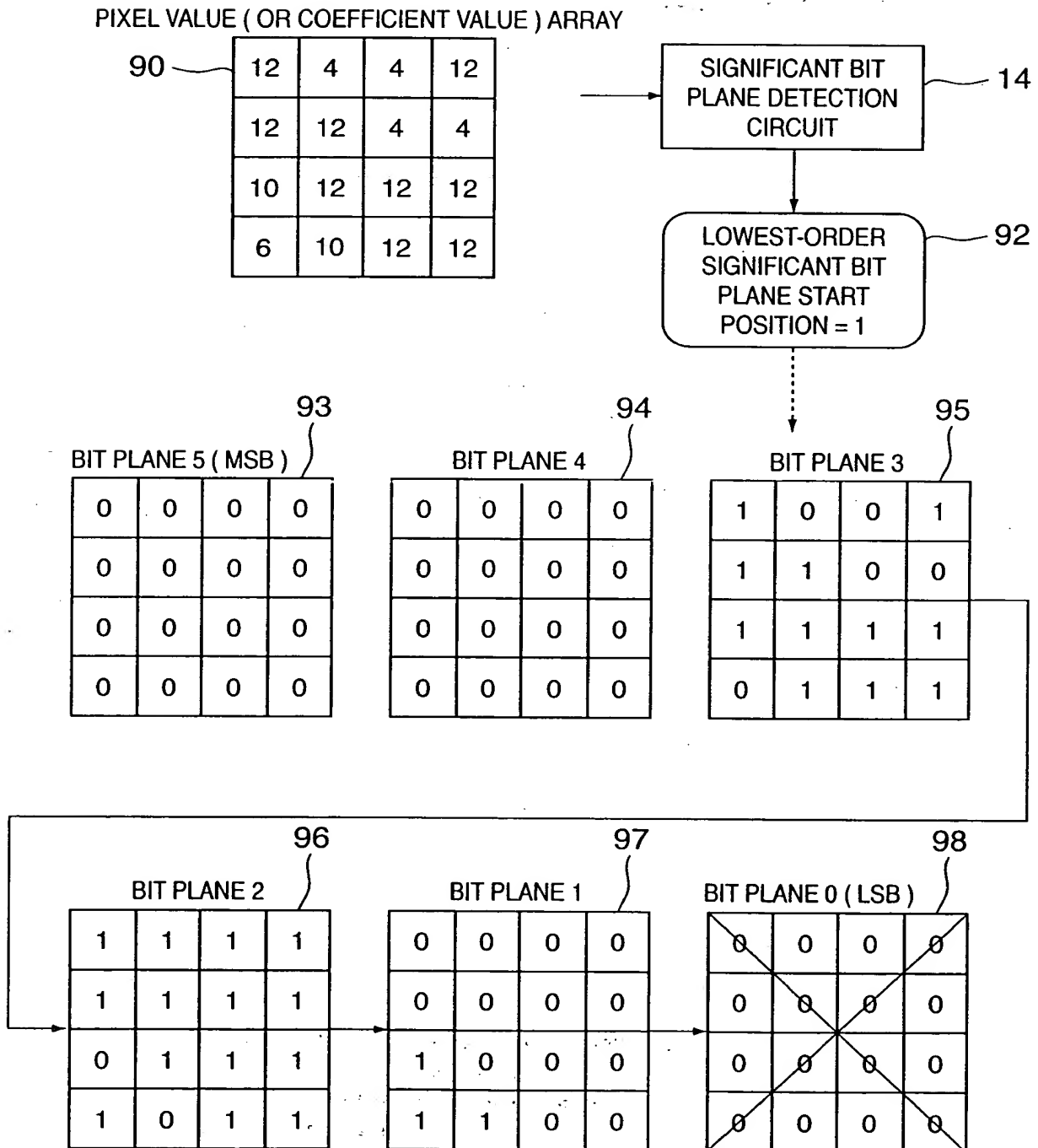
FIG. 8

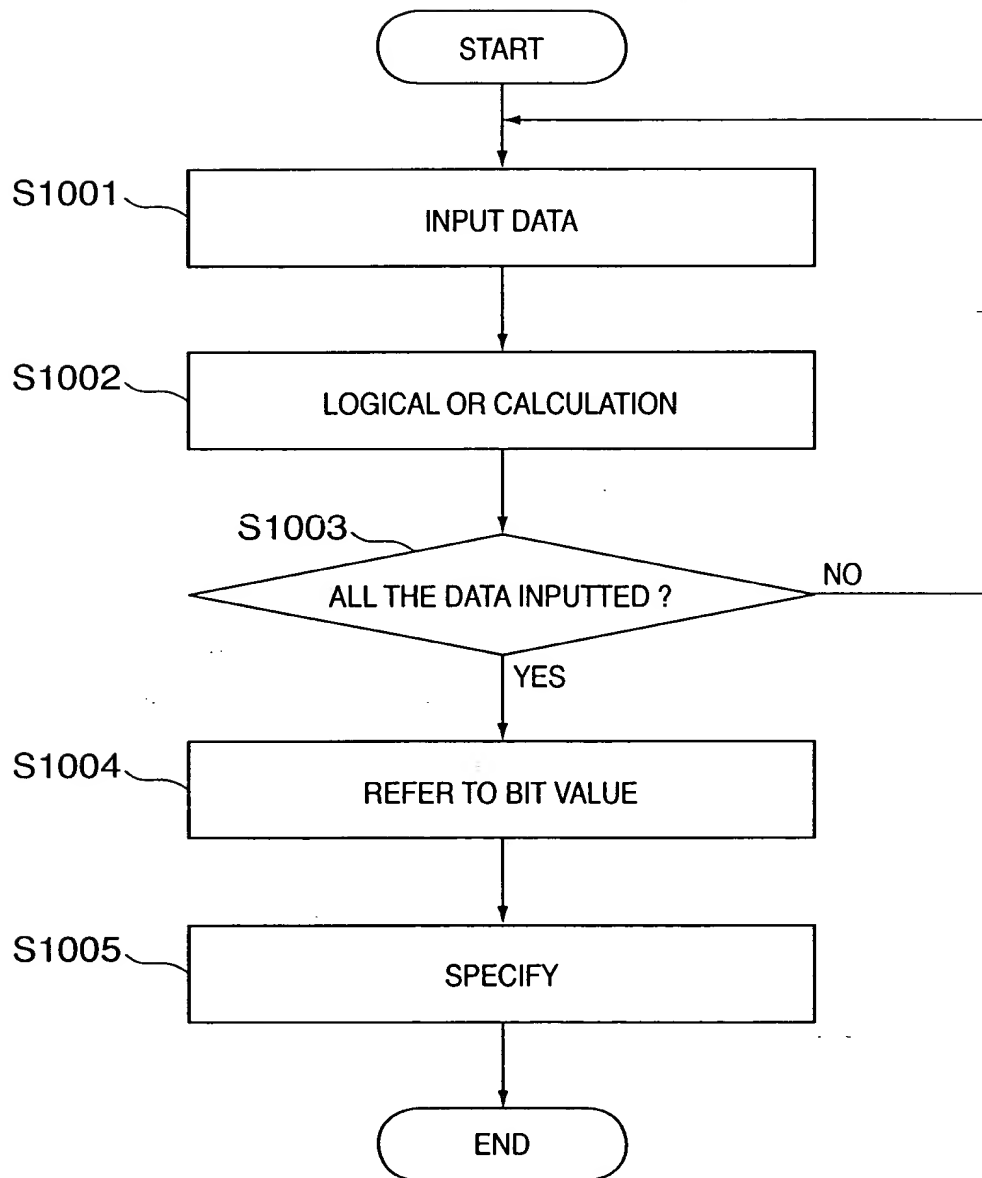


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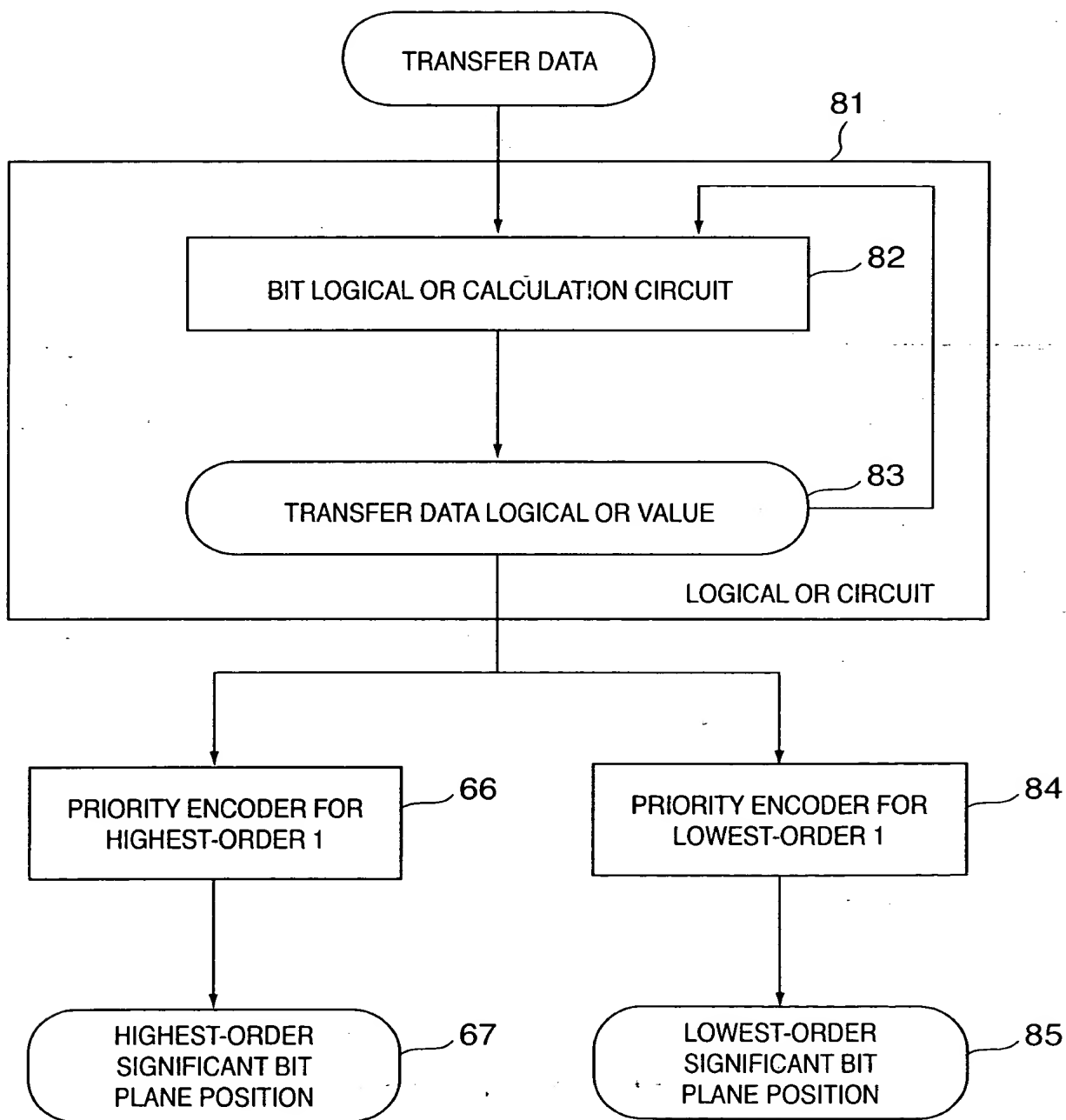
FIG. 9



**FIG. 10**

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FIG. 11



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